

The book was found

A Designer's Guide To Asynchronous VLSI



Synopsis

Bypass the limitations of synchronous design and create low power, higher performance circuits with shorter design times using this practical guide to asynchronous design. The fundamentals of asynchronous design are covered, as is a large variety of design styles, while the emphasis throughout is on practical techniques and real-world applications.

Book Information

Hardcover: 352 pages

Publisher: Cambridge University Press; 1 edition (March 15, 2010)

Language: English

ISBN-10: 0521872448

ISBN-13: 978-0521872447

Product Dimensions: 6.8 x 0.8 x 9.7 inches

Shipping Weight: 1.8 pounds (View shipping rates and policies)

Average Customer Review: 5.0 out of 5 stars 1 customer review

Best Sellers Rank: #2,706,758 in Books (See Top 100 in Books) #98 in [Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > VLSI & ULSI](#) #787 in [Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > Design](#) #1805 in [Books > Computers & Technology > Graphics & Design > CAD](#)

Customer Reviews

Bypass the limitations of synchronous design and create low power, higher performance circuits with shorter design times using this practical guide to asynchronous design. The fundamentals of asynchronous design are covered, as is a large variety of design styles, whilst emphasis throughout is placed on practical techniques and real-world applications.

Peter A. Beerel is CEO of TimeLess Design Automation - his own company commercializing asynchronous VLSI tools and libraries - and an Associate Professor in the Electrical Engineering Department at the University of Southern California (USC). Dr Beerel has 15 years experience of research and teaching in asynchronous VLSI and has received numerous awards including the VSoE Outstanding Teaching Award in 1997 and the 2008 IEEE Region 6 Outstanding Engineer Award for significantly advancing the application of asynchronous circuits to modern VLSI chips. Recep O. Ozdag is IC Design Manager at Fulcrum Microsystems and a part-time Lecturer at USC, where he received his Ph.D. in 2004. He is a recipient of the British Chevening Scholarship,

the Turkish Ministry of Education Post-Graduate Scholarship and the Turkish Higher Education Council Scholarship. Marcos Ferretti is one of the founders of PST Eletronica S. A. (Positron), Brazil - an automotive electronic systems manufacturing company, where he is currently Vice President of Technology. He received his Ph.D. from the University of Southern California (USC) in 2004 and was co-recipient of the USC Electrical Engineering-Systems Best Paper Award in the same year.

I've read this book during a research I'm conducting in the field of asynchronous VLSI. This is a wonderfully organized book: The book begins with a short review on the field of Asynchronous VLSI - what have been done in the field so far, what are the difficulties and what are the advantages and disadvantages of implementing asynchronous VLSI. Later on, several asynchronous handshaking protocols are described and explained, accompanied with examples and waveforms which really helped me understand some pretty complex ideas. In addition, VerilogCSP is introduced, providing the reader with an easy way to practice and get an "hands-on" experience with asynchronous VLSI (no need for propriety tools - any Verilog simulator will do!). The book continues with more complex concepts, like Micropipelines & QDI pipeline templates, which are explained very intuitively and are always followed by diagrams and exercises for the reader. All in all, I think it is a very good place to start with, if you are interested in the field of asynchronous VLSI. It helped me a lot and I really enjoyed reading it! I absolutely recommend it.

[Download to continue reading...](#)

A Designer's Guide to Asynchronous VLSI VLSI DESIGN SIMPLE AND LUCID EXPLANATION: vlsi design for students Circuits, Interconnections, and Packaging for Vlsi (Addison-Wesley VLSI systems series) Asynchronous Circuit Design The Architecture Of Light (2nd Edition): A textbook of procedures and practices for the Architect, Interior Designer and Lighting Designer. The Non-Designer's Design Book (Non Designer's Design Book) The Fashion Designer's Handbook & Fashion Kit: Learn to Sew and Become a Designer in 33 Fabulous Projects High Note 2018 Illustrated Orders of the Animals 18-Month Designer Wall Calendar: Unique, Beautifully Crafted, Featuring Unique, Original, Designer Art by Kelzuki (CHG0297) The Architecture Of Light: A textbook of procedures and practices for the Architect, Interior Designer and Lighting Designer. CMOS VLSI Design: A Circuits and Systems Perspective (4th Edition) VLSI Memory Chip Design (Springer Series in Advanced Microelectronics) (v. 5) Silicon VLSI Technology: Fundamentals, Practice, and Modeling Essentials of Electronic Testing for Digital, Memory and Mixed-Signal VLSI Circuits (Frontiers in Electronic Testing) PSPICE and MATLAB for Electronics: An Integrated Approach (VLSI Circuits) Silicon Processing for the VLSI Era, Vol. 1: Process Technology

Fundamentals of Modern VLSI Devices [Digital VLSI Chip Design with Cadence and Synopsys CAD Tools] By Brunvand, Erik (Author) [2009] [Paperback] Digital VLSI Chip Design with Cadence and Synopsys CAD Tools Silicon VLSI Technology VLSI Physical Design Automation: Theory and Practice

[Contact Us](#)

[DMCA](#)

[Privacy](#)

[FAQ & Help](#)